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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/709,811 05/28/2004 John S. Austin BUR920040136US1 3810 30449 7590 01/24/2006 **EXAMINER** SCHMEISER, OLSEN + WATTS CHANG, JOSEPH 3 LEAR JET LANE SUITE 201 **ART UNIT** PAPER NUMBER LATHAM, NY 12110 2817

DATE MAILED: 01/24/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

-		Application No.	Applicant(s)	
Office Action Summary		10/709,811	AUSTIN ET AL.	
		Examiner	Art Unit	
		Joseph Chang	2817	
Period fe	The MAILING DATE of this communication aported or Reply	pears on the cover sheet with the	e correspondence address	
WHIC - Exte after - If NC - Failt Any	HORTENED STATUTORY PERIOD FOR REPLICATION OF THE MAILING DEPOSITION OF	DATE OF THIS COMMUNICATI .136(a). In no event, however, may a reply be d will apply and will expire SIX (6) MONTHS fr te, cause the application to become ABANDO	ON. The timely filed From the mailing date of this communication. From the mailing date of this communication. From the mailing date of this communication.	
Status				
1) 🛛	Responsive to communication(s) filed on 17 I	November 2005		
′=	This action is FINAL . 2b) ☐ This action is non-final.			
3)				
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Disposit	ion of Claims		100 0.0, 2.0.	
	☑ Claim(s) <u>1-7,9-12,14,15 and 17-20</u> is/are pending in the application.			
7)63	4a) Of the above claim(s) is/are withdrawn from consideration.			
5)⊠	Claim(s) <u>1-5 and 7</u> is/are allowed.			
7)	Claim(s) <u>6, 9-12, 14, 15, 17-20</u> is/are rejected. Claim(s) is/are objected to.			
• =	•	or algetica requirement		
اــا(٥	Claim(s) are subject to restriction and/o	or election requirement.		
Applicat	ion Papers			
9)[The specification is objected to by the Examine	er.		
10)[10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.			
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. S	See 37 CFR 1.85(a).	
	Replacement drawing sheet(s) including the correct	ction is required if the drawing(s) is	objected to. See 37 CFR 1.121(d).	
11)[The oath or declaration is objected to by the E	xaminer. Note the attached Office	ce Action or form PTO-152.	
Priority (under 35 U.S.C. § 119			
	12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:			
,	 Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No Copies of the certified copies of the priority documents have been received in this National Stage 			
	application from the International Bureau (PCT Rule 17.2(a)).			
* 5	See the attached detailed Office action for a list	` ''	ived.	
Attachmen	rt(s)			
	æ of References Cited (PTO-892)	4) Interview Summa	ary (PTO-413)	
	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail	Date	
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08 PTO/SB/08)	6) Other:	ll Patent Application (PTO-152)	

Art Unit: 2817

DETAILED ACTION

Claim Rejections

The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claims 9-12, 14, 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Schorn, US Patent No. 6278334

Schorn discloses in Figures 1-4 an oscillator (Fig 1) and a method comprising: first (Vdd) and second (Ground) operating voltages, delay stages (4, 6, 8, 10 and 12), and inverting circuit (44), input node (IN), output node (OUT), a control circuit including a first switch circuit (upper transistor of 46) and a first resistance adjusting circuit (lower transistor of 46). It is noted that every element and connection are the same as the one in this application and therefore any functional limitations recited in the claims are inherently present.

Claims 6, 15 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Schorn in view of Abe et al.

Regarding claim 15, as discussed above, Schorn discloses an oscillator as recited in the claims except an extrinsic capacitor coupled between the output node and ground. As would have been well known in the art, capacitors or capacitive elements between the output node and ground are present whether intrinsically or extrinsically because the capacitance is a determination factor for the delay, for example, Abe et al. shows capacitor CL in Figure 5. Further, extrinsic capacitors are beneficial to an adjustment of amount of delays. Therefore, it would have been obvious to one of

Art Unit: 2817

ordinary skill in the art to place an extrinsic capacitor between the output node and ground because such a modification would have been beneficial to an adjustment of amount of delays.

Regarding claims 6 and 17, Although Schorn does not explicitly disclose that the inverter 44 is CMOS, one of ordinary skill in the art would have recognized that the inverter 44 is of CMOS because CMOS inverters are well known in the art, as an example, Figures 1-3 shows CMOS inverters.

Allowable Subject Matter

Claims 1-5, 7 are allowed.

The following is a statement of reasons for the indication of allowable subject matter: the best prior art of record, Schorn, taken alone or in combination of other references, does not teach or fairly suggest "the second resistance adjusting circuit comprises a first n-channel transistor"

Response to Arguments

Applicant's arguments with respect to claims 1-7, 9-12, 14, 15, 17-20 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

Application/Control Number: 10/709,811 Page 4

Art Unit: 2817

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph Chang whose telephone number is 571 272-1759. The examiner can normally be reached on Mon-Fri 0700-1730.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JØSEPH CHANG PRIMARY EXAMINER